

## Faculty Profile



**Name:** NARAYANAM RANGANADH

**Designation:** Assistant Professor, ECE

**Teaching Areas:** Digital Systems VLSI; Analog, Digital and Mixed CMOS VLSI; HDL programming; Nano-Technology; Electronic Devices and Circuits; Cellular Mobile Communications, Computer Organization & Architecture; speech processing.

**Research Interests:** DSP & DIP techniques and algorithms: software & hardware design and their specific applications into Neuroscience.

### Education:

- Master of Science in Electrical Engineering, 2001-2004, University of Texas at San Antonio, USA. (Research Option)
- B.Tech in Electronics and Communication Engineering, VR Siddhartha Engineering College, Nagarjuna University, 1996-2000
- Master of Business Administration, NIBM, 2016.

### Professional Experience: (8 years T + 12 years R)

- 2014 May - Till date: Faculty of Science & Technology, IFHE, Hyderabad.
- Research Training: Auditory Neural Signal Processing, 2009-2011, University of Ottawa, Canada.
- Signal & Image Processing Assistant III: Neural Signal and Image Processing, Helen Wills Neuro Science Institute, Henry Wheeler Jr. Brain Imaging Centre, 2004-2005, University of California Berkeley, USA.

### Research/Selected Publications:

1. Ranganadh Narayanam, "Translation Invariance (TI) based Novel Approach for better De-noising of Digital Images", IRJET, vol 4, Issue 3, March 2017.
2. Ranganadh Narayanam, Kishore Kumar, "A novel experimental evaluation for the 'development of a novel standard notion', image quality assessment (iqa) measures, computational time for image contrast enhancement", IJESRT, vol 5, Issue 8, August 2016.
3. Ranganadh Narayanam, "Developing 'standard novel 'VAD' technique' and 'noise free signals' for speech auditory brainstem responses for human subjects", IJESRT, vol 5, Issue 6, June 2016.
4. Ranganadh Narayanam, "Efficient De-noising performance of a combined algorithm of "Translation Invariant (TI) Wavelets and Independent Component Analysis" over "TI wavelets" for Speech-Auditory Brainstem Responses", Eleventh International Multi Conference on Information Processing, Bangalore, Elsevier-Procedia-computer Science International Journal, Vol 54, PP. 829-837, 2015.
5. Ranganadh N, P Patel, AM Grigoryan, "Performances of Texas instruments DSP and Xilinx FPGAs for Cooley-Tukey and Grigoryan FFT Algorithm", Wolters Kluwer-JET, Vol 1, Issue 2, 2011.

### Text Books:

6. Ranganadh Narayanam, "Implementation and Performance Evaluation of the Fast Discrete Fourier Transform by Using Radix-2 and Paired Transform Algorithms", 2004. (Google Books, Worldcat, MIT, Cornell Univ., University of Maryland)