

Faculty Profile



Name	Dr. S.S.S.P. Rao
Designation	Adjunct Professor
Teaching Areas:	Digital System design, Computer Organization and Architecture, Advance Computer Architecture, Microprocessors and Interfacing, Peripheral Devices and Interfacing, Field Programmable Gate Arrays
Research Interests	<i>Efficient Global and Detailed Routing Algorithm for Cell Based and Building Block Layout Designs Genetic Algorithms for VLSI Testing and ATPG An Integrated Approach for the Optimal Design of Digital Systems towards VLSI Realizations</i>

Education

- Ph.D. (CS) from Dept. of Computer Science and Engineering, IIT-Bombay
- M.Tech. in Applied Electronics from Dept of Electrical Engg., IIT BombaY
- D.M.I.T. in Electronics from Madras Institute of Technology, Chennai

Professional Experience:

Associate Lecturer	1966 - 69
Lecturer	1969 - 74
Assistant Professor	1974 - 83
Professor	1983 - 2004
Head, CSE Department	19th Aug 1985 - 10 June 1991
Ajunct Professor	January 2005 - July 2006
Chief Technology Officer, CMC-IDC, Hyderabad	January 2005 - 2nd October 2006
Chief Technology Officer, Xilinx, Hyderabad	3rd October 2006 - 31st March 2008
Chief Advisor and Mentor, CMC Limited, Hyderabad	1st April 2008 - 31st March 2015
Adjunct Professor, ICFAI Tech, Faculty of Science and Technology ICFAI, Hyderabad	1st April 2015 - Till date

Research/Selected Publications:

1. **S.S.S.P.Rao, J.R.Isaac**, "Interface Optimization: An Algorithm for the Detection of Data Path Redundancy and Reconfigurability Towards Obtaining Minimal Bus Interface, *IEEE Transactions on Computers*, Vol.38, No.11, Nov. 1989, pp. 1577-1579.
2. **S.S.S.P.Rao, J.R.Isaac**, "Micro-store width optimization through bit steering" *Microprocessing and Microprogramming*, Vol.16,No.1, Aug.1985 PP.17-30.
3. **S.S.S.P.Rao, V.Ramesh**, "Microstore Optimization based on Micro- operation utilization profile" *Microprocessing and Microprogramming*, Vol.13,No.2, Feb.1984, PP.115-120.

Books Published:

1. **S.S.S.P.Rao**, *Field Programmable Gate Arrays (FPGAs) and Applications*, Narosa Publishing House, 2015.
2. **S.S.S.P.Rao**, *Basics of Computer Organization and Architecture : Problems and Solutions*, Narosa Publishing House, 2014.
3. *Hall, Douglas V. and Rao S.S.S.P., MICROPROCESSORS AND INTERFACING 3rd Edition*, Tata McGraw Hill, 2012.