Faculty Profile

Name: **Dr. M V N Madhavi Latha**

Designation: Assistant Professor

Teaching Areas: Digital Electronics

Python

Data Communications

IoT

BA

Research Interest: RNS

Artificial Intelligence

Education: Ph.D-Visvesvaraya Technological University,Belagavi,2023

B.Tech- Narasaraopeta Engineering College,Narasaraopet,2004

M.Tech-CVR College of Engineering,Hyderabad,2009

Research/Selected Publications:

1. MVN.Madhavi Latha, Design of Reconfigurable Multipliers Based on High Speed Shannon Adders, International Journal of Engineering Research and Technology (IJERT), Vol. 1, Issue 7, September 2012.

2. MVN.Madhavi Latha, DivyaMadhuri, Ramesh,Analysis of Reconfigurable Multipliers for Integer and Galois Field Multiplication Based on High Speed Adders, International ConferenceOn Communication, Computing & Security (ICCCS), Proc. in Procedia Technology, Elsevier, Vol. 6, pp. 90-97,Oct 2012.

3. MVN.MadhaviLatha, Efficient Channel Estimation Technique for LTE Air Interface, Asia-Pacific Conference on Postgraduate Research in Microelectronics & Electronics (PrimeAsia), Proc. in IEEE,Dec 2012.

4. M. V. N. Madhavi Latha, Rashmi Racch and P. V. Ananda Mohan, "RNS-to-Binary Converters for a Three-Moduli Set {2n+k, 2n-1, 2n-1-1}", IETE journal of education, vol. 58, no. 1, pp. 20-28, 2017.

5. M. V. N. Madhavi Latha, Rachh, R. Rachh, and P.V. Ananda Mohan, "An efficient residue-to-binary converter for the moduli set {2n−1−1, 2n+k, 2n−1}" 2017 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics and Electronics. doi:10.1109/primeasia.2017.8280351

6. M. V. N. Madhavi Latha, Rachh, R. Rachh, and P.V. Ananda Mohan, "PhD Forum 2018 – Residue-to-Binary converters for the moduli set", Proceedings of the 24th International Conference on Advanced Computing and Communications (ADCOM 2018).

7. M. V. N. M. Latha, R. R. Rachh and P. V. A. Mohan, "Residue-to-Binary converters for the seven moduli set {2n-5-1, 2n-3-1, 2n-2+1, 2n-1-1, 2n-1+1, 2n, 2n+1} for n even," 2019 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics and Electronics (PrimeAsia), 2019, pp. 37-40, doi: 10.1109/PrimeAsia47521.2019.8950721

8. MVN. Madhavi Latha, Rashmi R Rachh, P.V. Anada Mohan," "Residue-to-Binary converter for seven moduli set {2n-5-1, 2n-3-1, 2n-2+1, 2n-1-1, 2n-1+1, 2n, 2n+1} for n Even" sadhana journal Sep. 2020.(Q2)

9. M. V. N. Madhavi Latha, Rachh, R. Rachh, and P.V. Ananda Mohan"Residue to Binary Converter for the extended four moduli set{2k, 2n-1 , 2n+1, 2n+1+1} for n odd sadhana journal feb-2023(Q2)

Research Projects:

- "INDO-KOREAN RESEARCH PROJECT- WIND TURBINES Collaborated with ARCHIMEDES GREEN ENERGYIS PRIVATE LIMITED" which was installed in GITAM Deemed to be University

- Involved in consultant work to a startup company wnp. The client is RCI , developing passive RADAR signal processing application (confidential)